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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Lukanc et al.

Serial No.: 10/790,381

Filed: March 1, 2004

Group Art Unit: 2825

Before the Examiner: Suchin Parihar

Title: SYSTEM AND METHOD FOR PRODUCING AN  
INTEGRATED CIRCUIT LAYOUT REPRESENTATION**REPLY UNDER 37 C.F.R. § 1.111**Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action having a mailing date of November 6, 2006, having a three-month shortened statutory period for response set to expire on February 6, 2007, please reconsider the rejections of the claims in view of the following remarks:

**Remarks** begin on page 2 of this paper.